



Entering VMX Operation

Hypervisor From Scratch

Second Part

Available At : Rayanfam.com/tutorials

Hi guys,

It's the second part of a multiple series of a tutorial called "Hypervisor From Scratch", First I highly recommend to read the [first part](#) (Basic Concepts & Configure Testing Environment) before reading this part, as it contains the basic knowledge you need to know in order to understand the rest of this tutorial.

In this section, we will learn about **Detecting Hypervisor Support** for our processor, then we simply config the basic stuff to **Enable VMX** and **Entering VMX Operation** and a lot more thing about **Window Driver Kit (WDK)**.

Configuring Our IRP Major Functions

Beside our kernel-mode driver ("MyHypervisorDriver"), I created a user-mode application called "MyHypervisorApp", first of all (The source code is available in my [GitHub](#)), I should encourage you to write most of your codes in user-mode rather than kernel-mode and that's because you might not have handled exceptions so it leads to BSODs, or on the other hand, running less code in kernel-mode reduces the possibility of putting some nasty kernel-mode bugs.

If you remember from the [previous part](#), we create some Windows Driver Kit codes, now we want to develop our project to support more IRP Major Functions.

IRP Major Functions are located in a conventional Windows table that is created for every device, once you register your device in Windows, you have to introduce these functions in which you handle these IRP Major Functions. That's like every device has a table of its Major Functions and everytime a user-mode application calls any of these functions, Windows finds the corresponding function (if device driver supports that MJ Function) based on the device that requested by the user and calls it then pass an IRP pointer to the kernel driver.

Now its responsibility of device function to check the privileges or etc.

The following code creates the device :

```
1 NTSTATUS NtStatus = STATUS_SUCCESS;
2 UINT64 uiIndex = 0;
3 PDEVICE_OBJECT pDeviceObject = NULL;
4 UNICODE_STRING usDriverName, usDosDeviceName;
5
6 DbgPrint("[*] DriverEntry Called.");
7
8 RtlInitUnicodeString(&usDriverName, L"\Device\MyHypervisorDevice");
9
10 RtlInitUnicodeString(&usDosDeviceName, L"\DosDevices\MyHypervisorDevice");
11
12 NtStatus = IoCreateDevice(pDriverObject, 0, &usDriverName, FILE_DEVICE_UNKNOWN,
13 NTSTATUS NtStatusSymLinkResult = IoCreateSymbolicLink(&usDosDeviceName, &usDrive
```

Note that our device name is “\Device\MyHypervisorDevice“.

After that, we need to introduce our Major Functions for our device.

```
1 if (NtStatus == STATUS_SUCCESS && NtStatusSymLinkResult == STATUS_SUCCESS)
2 {
3     for (uiIndex = 0; uiIndex < IRP_MJ_MAXIMUM_FUNCTION; uiIndex++)
4         pDriverObject->MajorFunction[uiIndex] = DrvUnsupported;
5
6     DbgPrint("[*] Setting Devices major functions.");
7     pDriverObject->MajorFunction[IRP_MJ_CLOSE] = DrvClose;
8     pDriverObject->MajorFunction[IRP_MJ_CREATE] = DrvCreate;
9     pDriverObject->MajorFunction[IRP_MJ_DEVICE_CONTROL] = DrvIOCTLDispatcher;
10    pDriverObject->MajorFunction[IRP_MJ_READ] = DrvRead;
11    pDriverObject->MajorFunction[IRP_MJ_WRITE] = DrvWrite;
12
13    pDriverObject->DriverUnload = DrvUnload;
14 }
15 else {
16     DbgPrint("[*] There was some errors in creating device.");
17 }
```

You can see that I put “**DrvUnsupported**” to all functions, this is a function to handle all MJ Functions and told the user that it’s not supported. The main body of this function is like this:

```
1 NTSTATUS DrvUnsupported(IN PDEVICE_OBJECT DeviceObject, IN PIRP Irp)
2 {
3     DbgPrint("[*] This function is not supported :( !");
4
5     Irp->IoStatus.Status = STATUS_SUCCESS;
6     Irp->IoStatus.Information = 0;
7     IoCompleteRequest(Irp, IO_NO_INCREMENT);
8
9     return STATUS_SUCCESS;
10 }
```

We also introduce other major functions that are essential for our device, we'll complete the implementation in the future, let's just leave them alone.

```
1 NTSTATUS DrvCreate(IN PDEVICE_OBJECT DeviceObject, IN PIRP Irp)
2 {
3     DbgPrint("[*] Not implemented yet :( !");
4
5     Irp->IoStatus.Status = STATUS_SUCCESS;
6     Irp->IoStatus.Information = 0;
7     IoCompleteRequest(Irp, IO_NO_INCREMENT);
8
9     return STATUS_SUCCESS;
10 }
11
12 NTSTATUS DrvRead(IN PDEVICE_OBJECT DeviceObject, IN PIRP Irp)
13 {
14     DbgPrint("[*] Not implemented yet :( !");
15
16     Irp->IoStatus.Status = STATUS_SUCCESS;
17     Irp->IoStatus.Information = 0;
18     IoCompleteRequest(Irp, IO_NO_INCREMENT);
19
20     return STATUS_SUCCESS;
21 }
22
23 NTSTATUS DrvWrite(IN PDEVICE_OBJECT DeviceObject, IN PIRP Irp)
24 {
25     DbgPrint("[*] Not implemented yet :( !");
26
27     Irp->IoStatus.Status = STATUS_SUCCESS;
28     Irp->IoStatus.Information = 0;
29     IoCompleteRequest(Irp, IO_NO_INCREMENT);
30
31     return STATUS_SUCCESS;
32 }
33
34 NTSTATUS DrvClose(IN PDEVICE_OBJECT DeviceObject, IN PIRP Irp)
35 {
36     DbgPrint("[*] Not implemented yet :( !");
37
38     Irp->IoStatus.Status = STATUS_SUCCESS;
39     Irp->IoStatus.Information = 0;
40     IoCompleteRequest(Irp, IO_NO_INCREMENT);
41
42     return STATUS_SUCCESS;
43 }
```

Now let's see IRP MJ Functions list and other types of Windows Driver Kit handlers routine.



IRP Major Functions List

This is a list of IRP Major Functions which we can use in order to perform different operations.

1 #define IRP_MJ_CREATE	0x00
2 #define IRP_MJ_CREATE_NAMED_PIPE	0x01
3 #define IRP_MJ_CLOSE	0x02
4 #define IRP_MJ_READ	0x03
5 #define IRP_MJ_WRITE	0x04
6 #define IRP_MJ_QUERY_INFORMATION	0x05
7 #define IRP_MJ_SET_INFORMATION	0x06
8 #define IRP_MJ_QUERY_EA	0x07
9 #define IRP_MJ_SET_EA	0x08
10 #define IRP_MJ_FLUSH_BUFFERS	0x09
11 #define IRP_MJ_QUERY_VOLUME_INFORMATION	0x0a
12 #define IRP_MJ_SET_VOLUME_INFORMATION	0x0b
13 #define IRP_MJ_DIRECTORY_CONTROL	0x0c
14 #define IRP_MJ_FILE_SYSTEM_CONTROL	0x0d
15 #define IRP_MJ_DEVICE_CONTROL	0x0e
16 #define IRP_MJ_INTERNAL_DEVICE_CONTROL	0x0f
17 #define IRP_MJ_SHUTDOWN	0x10
18 #define IRP_MJ_LOCK_CONTROL	0x11
19 #define IRP_MJ_CLEANUP	0x12
20 #define IRP_MJ_CREATE_MAILSLOT	0x13
21 #define IRP_MJ_QUERY_SECURITY	0x14
22 #define IRP_MJ_SET_SECURITY	0x15
23 #define IRP_MJ_POWER	0x16
24 #define IRP_MJ_SYSTEM_CONTROL	0x17
25 #define IRP_MJ_DEVICE_CHANGE	0x18
26 #define IRP_MJ_QUERY_QUOTA	0x19
27 #define IRP_MJ_SET_QUOTA	0x1a
28 #define IRP_MJ_PNP	0x1b
29 #define IRP_MJ_PNP_POWER	IRP_MJ_PNP // Obsolete....
30 #define IRP_MJ_MAXIMUM_FUNCTION	0x1b

Every major function will only trigger if we call its corresponding function from user-mode. For instance, there is a function (in user-mode) called **CreateFile** (And all its variants like **CreateFileA** and **CreateFileW** for **ASCII** and **Unicode**) so everytime we call **CreateFile** the function that registered as **IRP_MJ_CREATE** will be called or if we call **ReadFile** then **IRP_MJ_READ** and **WriteFile** then **IRP_MJ_WRITE** will be called. You can see that

Windows treats its devices like files and everything we need to pass from user-mode to kernel-mode is available in **PIRP Irp** as a buffer when the function is called.

In this case, Windows is responsible to copy user-mode buffer to kernel mode stack.

Don't worry we use it frequently in the rest of the project but we only support **IRP_MJ_CREATE** in this part and left others unimplemented for our future parts.

IRP Minor Functions

IRP Minor functions are mainly used for PnP manager to notify for a special event, for example, The PnP manager sends **IRP_MN_START_DEVICE** after it has assigned hardware resources, if any, to the device or The PnP manager sends **IRP_MN_STOP_DEVICE** to stop a device so it can reconfigure the device's hardware resources.

We will need these minor functions later in these series.

A list of IRP Minor Functions is available below:

```
1 IRP_MN_START_DEVICE
2 IRP_MN_QUERY_STOP_DEVICE
3 IRP_MN_STOP_DEVICE
4 IRP_MN_CANCEL_STOP_DEVICE
5 IRP_MN_QUERY_REMOVE_DEVICE
6 IRP_MN_REMOVE_DEVICE
7 IRP_MN_CANCEL_REMOVE_DEVICE
8 IRP_MN_SURPRISE_REMOVAL
9 IRP_MN_QUERY_CAPABILITIES
10 IRP_MN_QUERY_PNP_DEVICE_STATE
11 IRP_MN_FILTER_RESOURCE_REQUIREMENTS
12 IRP_MN_DEVICE_USAGE_NOTIFICATION
13 IRP_MN_QUERY_DEVICE_RELATIONS
14 IRP_MN_QUERY_RESOURCES
15 IRP_MN_QUERY_RESOURCE_REQUIREMENTS
16 IRP_MN_QUERY_ID
17 IRP_MN_QUERY_DEVICE_TEXT
18 IRP_MN_QUERY_BUS_INFORMATION
19 IRP_MN_QUERY_INTERFACE
20 IRP_MN_READ_CONFIG
21 IRP_MN_WRITE_CONFIG
22 IRP_MN_DEVICE_ENUMERATED
23 IRP_MN_SET_LOCK
```

Fast I/O

For optimizing VMM, you can use **Fast I/O** which is a different way to initiate I/O operations that are faster than IRP. Fast I/O operations are always synchronous.

According to [MSDN](#):

Fast I/O is specifically designed for rapid synchronous I/O on cached files. In fast I/O operations, data is transferred directly between user buffers and the system cache, bypassing the file system and the storage driver stack. (Storage drivers do not use fast I/O.) If all of the data to be read from a file is resident in the system cache when a fast I/O read or write request is received, the request is satisfied immediately.

When the I/O Manager receives a request for synchronous file I/O (other than paging I/O), it invokes the fast I/O routine first. If the fast I/O routine returns **TRUE**, the operation was serviced by the fast I/O routine. If the fast I/O routine returns **FALSE**, the I/O Manager creates and sends an IRP instead.

The definition of Fast I/O Dispatch table is:

```
1 typedef struct _FAST_IO_DISPATCH {
2     ULONG
3     PFAST_IO_CHECK_IF_POSSIBLE
4     PFAST_IO_READ
5     PFAST_IO_WRITE
6     PFAST_IO_QUERY_BASIC_INFO
7     PFAST_IO_QUERY_STANDARD_INFO
8     PFAST_IO_LOCK
9     PFAST_IO_UNLOCK_SINGLE
10    PFAST_IO_UNLOCK_ALL
11    PFAST_IO_UNLOCK_ALL_BY_KEY
12    PFAST_IO_DEVICE_CONTROL
13    PFAST_IO_ACQUIRE_FILE
14    PFAST_IO_RELEASE_FILE
15    PFAST_IO_DETACH_DEVICE
16    PFAST_IO_QUERY_NETWORK_OPEN_INFO
17    PFAST_IO_ACQUIRE_FOR_MOD_WRITE
18    PFAST_IO_MDL_READ
19    PFAST_IO_MDL_READ_COMPLETE
20    PFAST_IO_PREPARE_MDL_WRITE
21    PFAST_IO_MDL_WRITE_COMPLETE
22    PFAST_IO_READ_COMPRESSED
23    PFAST_IO_WRITE_COMPRESSED
24    PFAST_IO_MDL_READ_COMPLETE_COMPRESSED
25    PFAST_IO_MDL_WRITE_COMPLETE_COMPRESSED
26    PFAST_IO_QUERY_OPEN
27    PFAST_IO_RELEASE_FOR_MOD_WRITE
28    PFAST_IO_ACQUIRE_FOR_CCFLUSH
29    PFAST_IO_RELEASE_FOR_CCFLUSH
30 } FAST_IO_DISPATCH, *PFAST_IO_DISPATCH;
```

Defined Headers

I created the following headers (source.h) for my driver.

```
1 #pragma once
2 #include <ntddk.h>
3 #include <wdf.h>
4 #include <wdm.h>
5
6 extern void inline Breakpoint(void);
7 extern void inline Enable_VMX_Operation(void);
8
9
10 NTSTATUS DriverEntry(PDRIVER_OBJECT pDriverObject, PUNICODE_STRING pRegistryPath
11 VOID DrvUnLoad(PDRIVER_OBJECT DriverObject);
12 NTSTATUS DrvCreate(IN PDEVICE_OBJECT DeviceObject, IN PIRP Irp);
13 NTSTATUS DrvRead(IN PDEVICE_OBJECT DeviceObject, IN PIRP Irp);
14 NTSTATUS DrvWrite(IN PDEVICE_OBJECT DeviceObject, IN PIRP Irp);
15 NTSTATUS DrvClose(IN PDEVICE_OBJECT DeviceObject, IN PIRP Irp);
16 NTSTATUS DrvUnsupported(IN PDEVICE_OBJECT DeviceObject, IN PIRP Irp);
17 NTSTATUS DrvIOCTLDispatcher(IN PDEVICE_OBJECT DeviceObject, IN PIRP Irp);
18
19 VOID PrintChars(_In_reads_(CountChars) PCHAR BufferAddress, _In_ size_t CountChars
20 VOID PrintIrpInfo(PIRP Irp);
21
22 #pragma alloc_text(INIT, DriverEntry)
23 #pragma alloc_text(PAGE, DrvUnload)
24 #pragma alloc_text(PAGE, DrvCreate)
25 #pragma alloc_text(PAGE, DrvRead)
```

```

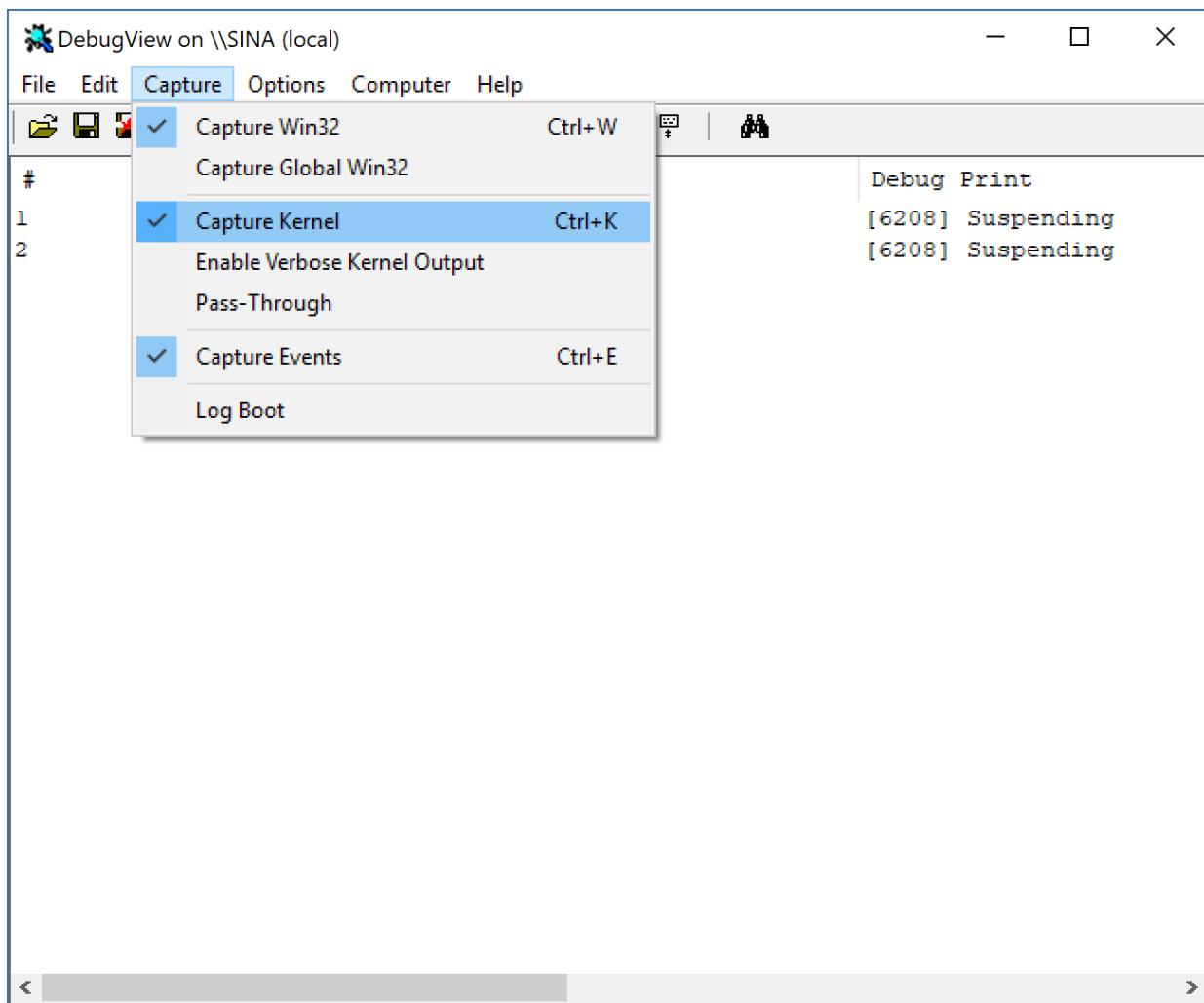
26 #pragma alloc_text(PAGE, DrvWrite)
27 #pragma alloc_text(PAGE, DrvClose)
28 #pragma alloc_text(PAGE, DrvUnsupported)
29 #pragma alloc_text(PAGE, DrvIOCTLDispatcher)
30
31
32
33 // IOCTL Codes and Its meanings
34 #define IOCTL_TEST 0x1 // In case of testing

```

Now just compile your driver.

Loading Driver and Check the presence of Device

In order to load our driver (MyHypervisorDriver) first download OSR Driver Loader, then run Sysinternals DbgView as administrator make sure that your DbgView captures the kernel (you can check by going Capture -> Capture Kernel).



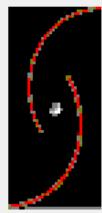
After that open the OSR Driver Loader (go to OsrLoader -> kit-> WNET -> AMD64 -> FRE) and open OSRLOADER.exe (in an x64 environment). Now if you built your driver, find .sys file (in MyHypervisorDriver\x64\Debug\ should be a file named: "MyHypervisorDriver.sys"), in OSR Driver Loader click to browse and select (MyHypervisorDriver.sys) and then click to "Register Service" after the message box that shows your driver registered successfully, you should click on "Start Service".

Please note that you should have **WDK** installed for your Visual Studio in order to be able building your project.



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Ver: V3.0 - Sept 6, 2007

[Exit](#)[Help](#)[ServiceGroupOrder](#)[Active Services](#)

Registry Key: yourdrivername

Driver Path: \ypervisordriver\x64\Debug\MyHypervisorDriver.sys [Browse](#)

Driver Version:

Driver Size:

Driver File Time:

Display Name: yourdrivername

Service Start: Demand

Load Group: None

[Group Load Order](#)

Order In Group: 1 Type: Driver Error: Normal

Depend On Group(s):

- AudioGroup
- Base
- Boot Bus Extender
- Boot File System

Last Status:

MiniFilter Settings

Default Instance:

Altitude:

0

AltitudeAndFlags

Flags:

0

[Register Service](#)[Unregister Service](#)[Start Service](#)[Stop Service](#)

Now come back to DbgView, then you should see that your driver loaded successfully and a message “[*] **DriverEntry Called.**” should appear.

If there is no problem then you’re good to go, otherwise, if you have a problem with DbgView you can check the next step.

Keep in mind that now you registered your driver so you can use **SysInternals WinObj** in order to see whether “**MyHypervisorDevice**” is available or not.

Name /	Type	
[] ArcName	SymbolicLink	\Device\MacHALDriver
[] BaseNamedObjects	SymbolicLink	\Device\MailSlot
[] Callback	SymbolicLink	\Device\MountPointManager
[] Device	SymbolicLink	\Device\MP
[] Driver	SymbolicLink	\Device\MSSECFLTSYS
[] DriverStores	SymbolicLink	\Device\MSGRMAGENTSYS
[] FileSystem	SymbolicLink	\Device\MyHypervisorDevice
[] GLOBAL???	SymbolicLink	\Device\nativewifip
[] KernelObjects	SymbolicLink	\Device\Ndis
[] KnownDlIs32	SymbolicLink	\Device\NdisVan
[] NLS	SymbolicLink	\Device\NDProxy
[] ObjectTypes	SymbolicLink	\Device\NduIoDevice
[] RPC Control	SymbolicLink	\Device\NPF_04342DBC-514B-48CA-85C4-2AB10B5...
[] Security	SymbolicLink	\Device\NPF_0471EF45-18B4-4B60-AB8B-184F7C50...
[] Sessions	SymbolicLink	\Device\NPF_0F23C37A-ADF7-4EB0-8B75-DC3DBF1...
[] UMDFCommunicationPorts	SymbolicLink	\Device\NPF_2097DBF0-B39C-435B-B5EE-232DD079...
[] Windows	SymbolicLink	\Device\NPF_2BFE1E80-DC12-4627-97C6-B7153A0...
[] NPF_0325D7DAE-89CF...	SymbolicLink	\Device\NPF_325D7DAE-89CF-4B70-A55E-2C76EB4...
[] NPF_3B047984-2808...	SymbolicLink	\Device\NPF_3B047984-2808-4F20-ABBA-DE50A...
[] NPF_55B002A9-3C2C...	SymbolicLink	\Device\NPF_55B002A9-3C2C-4015-92B2-25921497...
[] NPF_59140177-FF73...	SymbolicLink	\Device\NPF_59140177-FF73-47CC-B241-82019C99F...
[] NPF_688284E7-0D68...	SymbolicLink	\Device\NPF_688284E7-0D68-4FA0-8886-099CFD4...
[] NPF_95793F34-1FB5...	SymbolicLink	\Device\NPF_95793F34-1FB5-4D09-8D04-EF14B77...
[] NPF_A175D6D3-4BD1...	SymbolicLink	\Device\NPF_A175D6D3-4BD1-4F71-A58E-3586AFFE...
[] NPF_A4A8C432-0195...	SymbolicLink	\Device\NPF_A4A8C432-0195-494C-984A-6017478A...
[] NPF_B89725844-4CEA...	SymbolicLink	\Device\NPF_B89725844-4CEA-4BF0-80E9-4A03FD4B...
[] NPF_C2A241AD-FFC...	SymbolicLink	\Device\NPF_C2A241AD-FFC-481A-87FE-6FC3C...
[] NPF_D0385365-0239...	SymbolicLink	\Device\NPF_D0385365-0239-4682-9564-0200C236...
[] NPF_E6360328-F17A...	SymbolicLink	\Device\NPF_E6360328-F17A-45FB-8899-283FB25...
[] NPF_E9951840-1CDC...	SymbolicLink	\Device\NPF_E9951840-1CDC-4ECC-B7A8-8F66C5A...
[] Nsi	SymbolicLink	\Device\Nsi
[] Ntfs	SymbolicLink	\Ntfs
[] NUL	SymbolicLink	\Device\Null
[] NXTIPSECDevice	SymbolicLink	\Device\NXTIPSEC
[] PartmgrControl	SymbolicLink	\Device\PartmgrControl
[] PCI#VEN_1002&DEV_...	SymbolicLink	\Device\NTPNP_PCI0018
[] PCI#VEN_1002&DEV_...	SymbolicLink	\Device\NTPNP_PCI0020
[] PCI#VEN_144D&DEV_...	SymbolicLink	\Device\NTPNP_PCI0018
[] PCI#VEN_144D&DEV_...	SymbolicLink	\Device\NTPNP_PCI0020

\GLOBAL???\ACPI#GenuineIntel_{Intel64_Family_6_Model_94_Intel(R)_Core(TM)_7-6820HQ_CPU_@_2.70GHz\#_6#dbe4373d-3c81-40cb-ace4-e0e5d05f0c9f

The Problem with DbgView

Unfortunately, for some unknown reasons, I'm not able to view the result of `DbgPrint()`, If you can see the result then you can skip this step but if you have a problem, then perform the following steps:

As I mentioned in [part 1](#):

In regedit, add a key:

HKEY_LOCAL_MACHINE\SYSTEM\CurrentControlSet\Control\Session Manager\Debug Print Filter

Under that , add a DWORD value named IHVDRIVER with a value of 0xFFFF

Reboot the machine and you'll good to go.

It always works for me and I tested on many computers but my MacBook seems to have a problem.

In order to solve this problem, you need to find a Windows Kernel Global variable called, **nt!Kd_DEFAULT_Mask**, this variable is responsible for showing the results in DbgView, it has a mask that I'm not aware of so I just put a `0xffffffff` in it to simply make it shows everything!

To do this, you need a Windows Local Kernel Debugging using Windbg.

1. Open a Command Prompt window as Administrator. Enter **bcdedit /debug on**
2. If the computer is not already configured as the target of a debug transport, enter **bcdedit /dbgsettings local**

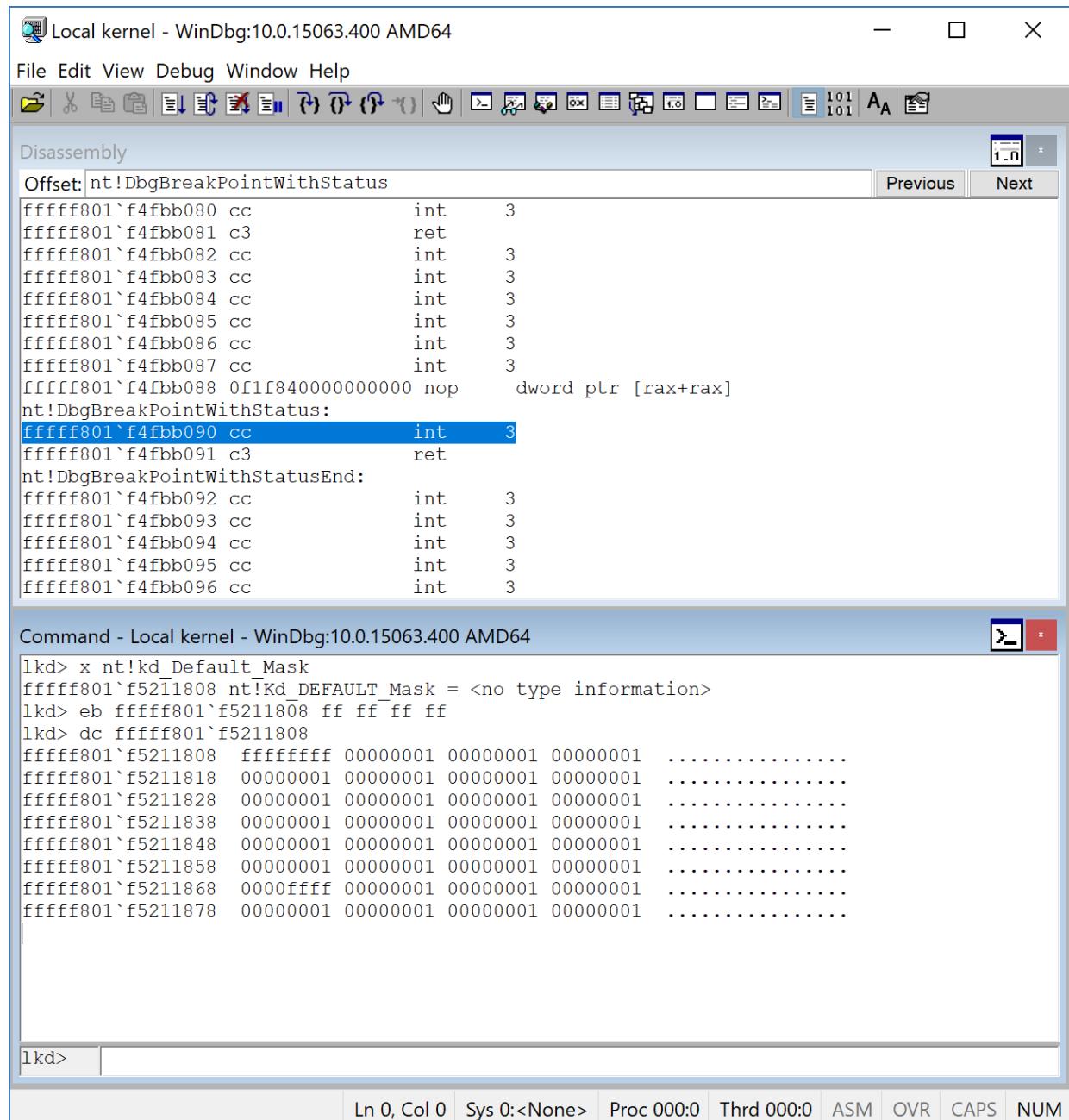
3. Reboot the computer.

After that you need to open Windbg with UAC Administrator privilege, go to File > Kernel Debug > Local > press OK and in your local Windbg find the **nt!Kd_DEFAULT_Mask** using the following command :

```
1 prlkd> x nt!kd_Default_Mask  
2 fffff801`f5211808 nt!Kd_DEFAULT_Mask = <no type information>
```

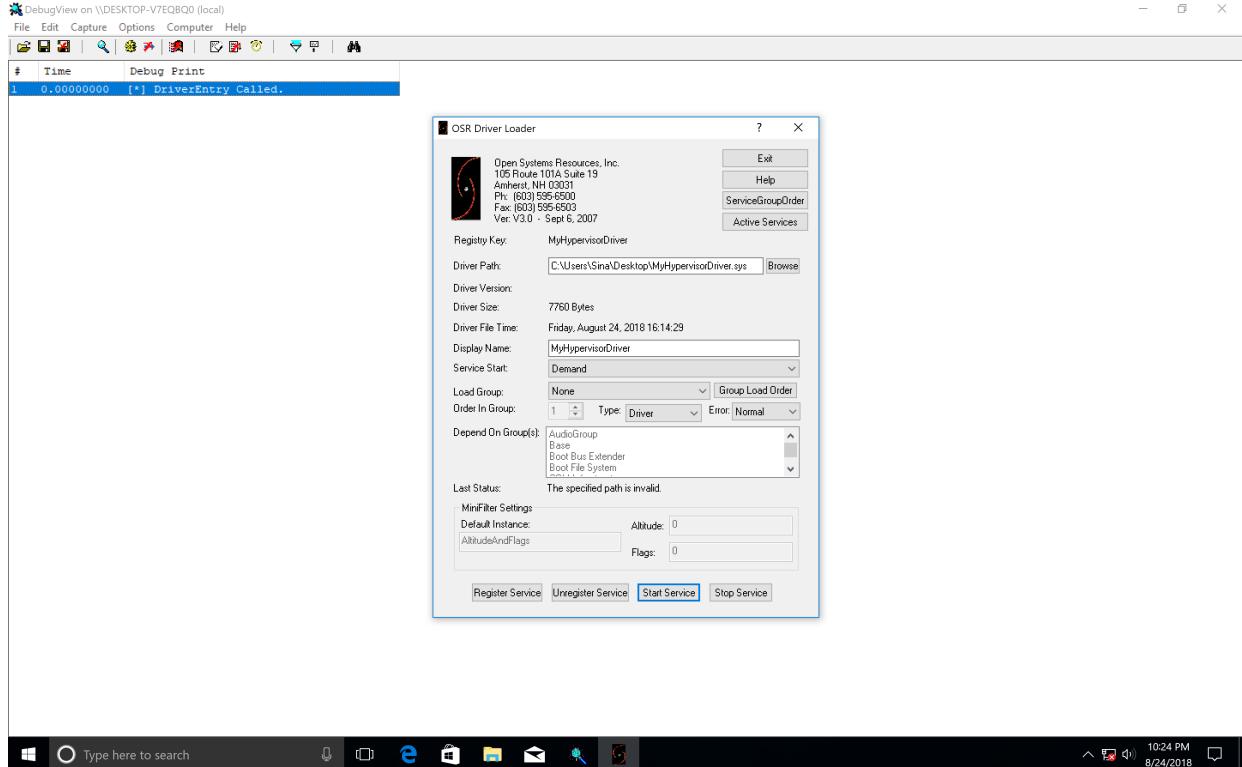
Now change its value to 0xffffffff.

```
1 lkd> eb fffff801`f5211808 ff ff ff ff
```



After that, you should see the results and now you'll be good to go.

Remember this is an essential step for the rest of the topic, because if we can't see any kernel detail then we can't debug.



Detecting Hypervisor Support

Discovering support for **vmx** is the first thing that you should consider before enabling **VT-x**, this is covered in **Intel Software Developer's Manual volume 3C** in section **23.6 DISCOVERING SUPPORT FOR VMX.**

You could know the presence of VMX using **CPUID if CPUID.1:ECX.VMX[bit 5] = 1**, then VMX operation is supported.

First of all, we need to know if we're running on an Intel-based processor or not, this can be understood by checking the CPUID instruction and find vendor string “**GenuineIntel**“.

The following function returns the vendor string form CPUID instruction.

```

1 string GetCpuID()
2 {
3     //Initialize used variables
4     char SysType[13]; //Array consisting of 13 single bytes/characters
5     string CpuID; //The string that will be used to add all the characters to
6     //Starting coding in assembly language
7     _asm
8     {
9         //Execute CPUID with EAX = 0 to get the CPU producer
10        XOR EAX, EAX
11        CPUID
12        //MOV EBX to EAX and get the characters one by one by using shift out right bitw
13        MOV EAX, EBX
14        MOV SysType[0], al
15        MOV SysType[1], ah
16        SHR EAX, 16
17        MOV SysType[2], al
18        MOV SysType[3], ah

```

```

19 //Get the second part the same way but these values are stored in EDX
20 MOV EAX, EDX
21 MOV SysType[4], al
22 MOV SysType[5], ah
23 SHR EAX, 16
24 MOV SysType[6], al
25 MOV SysType[7], ah
26 //Get the third part
27 MOV EAX, ECX
28 MOV SysType[8], al
29 MOV SysType[9], ah
30 SHR EAX, 16
31 MOV SysType[10], al
32 MOV SysType[11], ah
33 MOV SysType[12], 00
34 }
35 CpuID.assign(SysType, 12);
36 return CpuID;
37 }
```

The last step is checking for the presence of VMX, you can check it using the following code :

```

1 bool VMX_Support_Detection()
2 {
3
4     bool VMX = false;
5     __asm {
6         xor    eax, eax
7         inc    eax
8         cpuid
9         bt     ecx, 0x5
10        jc    VMXSupport
11        VMXNotSupport :
12        jmp   NopInstr
13        VMXSupport :
14        mov   VMX, 0x1
15        NopInstr :
16        nop
17    }
18
19    return VMX;
20 }
```

As you can see it checks CPUID with EAX=1 and if the 5th (6th) bit is 1 then the VMX Operation is supported. We can also perform the same thing in Kernel Driver.

All in all, our main code should be something like this:

```

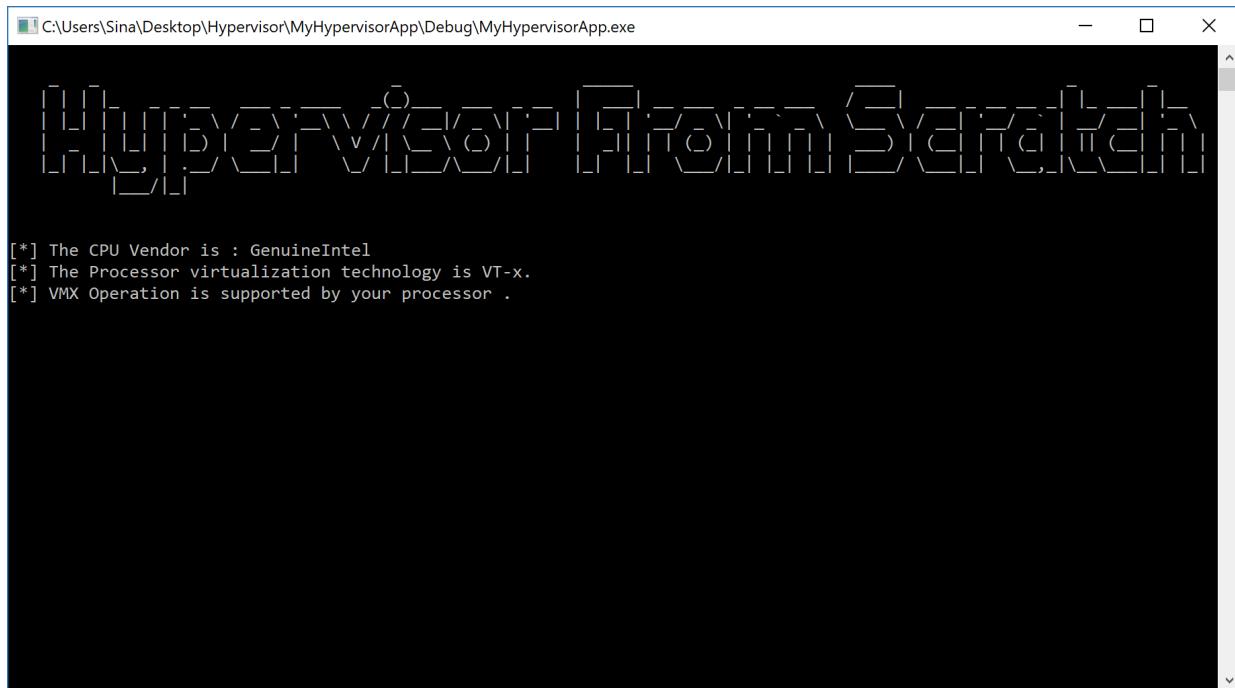
1 int main()
2 {
3     string CpuID;
4     CpuID = GetCpuID();
5     cout << "[*] The CPU Vendor is : " << CpuID << endl;
6     if (CpuID == "GenuineIntel")
7     {
8         cout << "[*] The Processor virtualization technology is VT-x. \n";
9     }
10    else
11    {
12        cout << "[*] This program is not designed to run in a non-VT-x environment !\n";
13        return 1;
14    }
15
16    if (VMX_Support_Detection())
17    {
18        cout << "[*] VMX Operation is supported by your processor .\n";
19    }
20    else
```

```

21 {
22     cout << "[*] VMX Operation is not supported by your processor .\n";
23     return 1;
24 }
25 _getch();
26     return 0;
27 }

```

The final result:



Enabling VMX Operation

If our processor supports the VMX Operation then its time to enable it. As I told you above, **IRP_MJ_CREATE** is the first function that should be used to start the operation.

Form Intel Software Developer's Manual (**23.7 ENABLING AND ENTERING VMX OPERATION**):

Before system software can enter VMX operation, it enables VMX by setting CR4.VMXE[bit 13] = 1. VMX operation is then entered by executing the VMXON instruction. VMXON causes an invalid-opcode exception (#UD) if executed with CR4.VMXE = 0. Once in VMX operation, it is not possible to clear CR4.VMXE. System software leaves VMX operation by executing the VMXOFF instruction. CR4.VMXE can be cleared outside of VMX operation after executing of VMXOFF.

VMXON is also controlled by the IA32_FEATURE_CONTROL MSR (MSR address 3AH). This MSR is cleared to zero when a logical processor is reset. The relevant bits of the MSR are:

- Bit 0 is the lock bit. If this bit is clear, VMXON causes a general-protection exception. If the lock bit is set, WRMSR to this MSR causes a general-protection exception; the MSR cannot be modified until a power-up reset condition. System BIOS can use this bit to

provide a setup option for BIOS to disable support for VMX. To enable VMX support in a platform, BIOS must set bit 1, bit 2, or both, as well as the lock bit.

- Bit 1 enables VMXON in SMX operation. If this bit is clear, execution of VMXON in SMX operation causes a general-protection exception. Attempts to set this bit on logical processors that do not support both VMX operation and SMX operation cause general-protection exceptions.
- Bit 2 enables VMXON outside SMX operation. If this bit is clear, execution of VMXON outside SMX operation causes a general-protection exception. Attempts to set this bit on logical processors that do not support VMX operation cause general-protection exceptions.

Setting CR4 VMXE Bit

Do you remember the previous part where I told you how to [create an inline assembly in Windows Driver Kit x64](#)?

Now you should create some function to perform this operation in assembly.

Just in Header File (in my case **Source.h**) declare your function:

```
1 extern void inline Enable_VMX_Operation(void);
```

Then in assembly file (in my case SourceAsm.asm) add this function (Which set the 13th (14th) bit of Cr4).

```
1 Enable_VMX_Operation PROC PUBLIC
2 push rax ; Save the state
3
4 xor rax,rax ; Clear the RAX
5 mov rax,cr4
6 or rax,02000h      ; Set the 14th bit
7 mov cr4,rax
8
9 pop rax ; Restore the state
10 ret
11 Enable_VMX_Operation ENDP
```

Also, declare your function in the above of SourceAsm.asm.

```
1 PUBLIC Enable_VMX_Operation
```

The above function should be called in **DrvCreate**:

```
1 NTSTATUS DrvCreate(IN PDEVICE_OBJECT DeviceObject, IN PIRP Irp)
2 {
3     Enable_VMX_Operation(); // Enabling VMX Operation
4     DbgPrint("[*] VMX Operation Enabled Successfully !");
5     return STATUS_SUCCESS;
6 }
```

At last, you should call the following function from the user-mode:

```
1 HANDLE hWnd = CreateFile(L"\\\\.\\"MyHypervisorDevice",
2 GENERIC_READ | GENERIC_WRITE,
```

```

3 FILE_SHARE_READ |  

4 FILE_SHARE_WRITE,  

5 NULL, /// lpSecurityAttributes  

6 OPEN_EXISTING,  

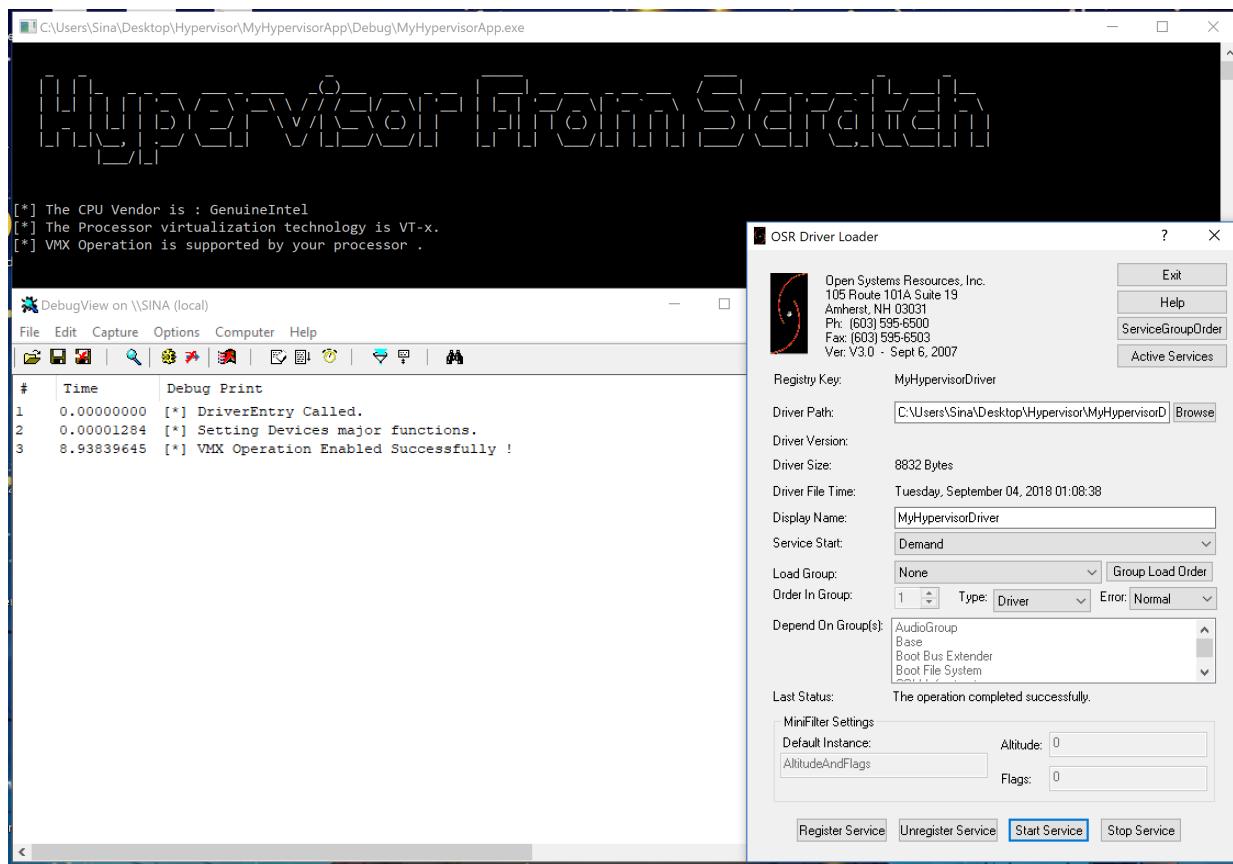
7 FILE_ATTRIBUTE_NORMAL |  

8 FILE_FLAG_OVERLAPPED,  

9 NULL); /// lpTemplateFile

```

If you see the following result, then you completed the second part successfully.



Important Note: Please consider that your .asm file should have a different name from your driver main file (.c file) for example if your driver file is “Source.c” then using the name “Source.asm” causes weird linking errors in Visual Studio, you should change the name of your .asm file to something like “SourceAsm.asm” to avoid these kinds of linker errors.

Conclusion

In this part, you learned about basic stuff you to know in order to create a Windows Driver Kit program and then we entered to our virtual environment so we build a cornerstone for the rest of the parts.

In the third part, we’re getting deeper with Intel VT-x and make our driver even more advanced so wait, it’ll be ready soon!

The source code of this topic is available at :

[<https://github.com/SinaKarvandi/Hypervisor-From-Scratch/>]



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Sinaei

Judas tree , What kind of mystery is this, that every spring, Comes with our hearts' mourning, Judas tree, You be elate, You sing my unsang song...



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